

1 **AMENDMENTS TO THE CLAIMS**

2 Please amend the claims of the present application as set forth in the listing
3 of claims below. This listing of claims will replace all prior versions, and listings, of
4 claims in the application.

5 Claims 1-52 were originally filed.

6 Claims 45 – 52 have been withdrawn.

7 Claims 7 - 14, 21, and 27 have been cancelled.

8 Claims 1, 2, 15, 36, and 38 have been amended.

9 Accordingly, claims 1 – 6, 15 – 20, 22 – 26, and 28 - 44 are pending.
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12 **LISTING OF CLAIMS**

13 1. **(Currently Amended)** A method comprising:
14 receiving a request to write data to a logical sector address of a flash
15 memory medium;
16 selecting a physical sector address from a list of free physical sector
17 addresses;
18 assigning [[a]] the selected free physical sector address to the logical sector
19 address forming a corresponding relationship between the addresses;
20 storing the corresponding relationship between the addresses in a data
21 structure; and
22 writing the data into a physical sector of the flash memory medium at a
23 location indicated by the selected free physical sector address.
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2 2. **(Currently Amended)** The method as recited in Claim 1, wherein the
3 data structure is ~~contained in at least one memory device other than~~ stored on the
4 flash memory medium.

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6 3. **(Original)** The method as recited in Claim 1, wherein the data structure
7 is contained in a random access memory device.
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10 4. **(Original)** The method as recited in Claim 1, wherein the request is
11 received from a file system.
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13 5. **(Original)** The method as recited in Claim 1, further comprising storing
14 the logical sector address in the physical sector of the flash memory medium along
15 with the data.
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18 6. **(Original)** The method as recited in Claim 1, further comprising:
19 storing the logical sector address in the physical sector of the flash memory
20 medium along with the data;

21 if the data structure is erased, then scanning the flash memory medium to
22 locate the logical sector address stored with the data;
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1 assigning the physical sector address containing the data to the logical
2 sector address forming a reestablished corresponding relationship between the
3 addresses; and

4 storing the reestablished corresponding relationship between the addresses
5 in a new data structure.

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7 7 - 14. (Canceled)

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10 15. (Currently Amended) ~~A method comprising~~ A computer-readable
11 medium having computer-executable instructions for performing steps
12 comprising:

13 receiving a request to write data to a logical sector address of a flash
14 memory medium;

15 selecting a physical sector of the flash memory medium to store the data
16 based on the ability of the physical sector to store the data without first being
17 erased;

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19 assigning a physical sector address of the selected physical sector to the
20 logical sector address forming a corresponding relationship between the addresses;

21 storing the corresponding relationship between the addresses in a data
22 structure;

23 writing the data into a the physical sector of the flash memory medium at a
24 location indicated by the physical sector address; and
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1 writing the logical sector address in the physical sector of the flash memory
2 medium along with the data.
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4 16. **(Original)** The method as recited in Claim 15, further comprising:

5 if the data structure is erased, then scanning the flash memory medium to
6 locate the logical sector address stored with the data;

7 assigning the physical sector address containing the data to the logical
8 sector address forming a reestablished corresponding relationship between the
9 addresses; and
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11 storing the reestablished corresponding relationship between the addresses
12 in a new data structure.
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14 17. **(Original)** The method as recited in Claim 15, wherein writing the
15 logical sector address in the physical sector of the flash memory medium includes
16 writing the logical sector address into a spare portion of the physical sector.
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19 18. **(Original)** The method as recited in Claim 15, wherein the data structure
20 is contained in at least one memory device other than the flash memory medium.
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22 19. **(Original)** The method as recited in Claim 15, wherein the data structure
23 is contained in a random access memory device.
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20. **(Original)** The method as recited in Claim 15, wherein the request is received from a file system.

21. **(Canceled)**

22. **(Original)** A method comprising:

(a) receiving a request to write data to a logical sector address of a flash memory medium;

(b) assigning a physical sector address to the logical sector address forming a corresponding relationship between the addresses;

(c) storing the corresponding relationship between the addresses in a data structure;

(d) writing the data into a physical sector of the flash memory medium at a location indicated by the physical sector address;

(e) receiving a request to rewrite updated data to the logical sector address;

(f) assigning a new physical sector address to the logical sector address forming a corresponding relationship between the new physical sector address and the logical sector address;

(g) storing the corresponding relationship between the addresses from the aforementioned paragraph (f) in the data structure;

1 (h) writing the updated data into a physical sector of the flash memory
2 medium at a location indicated by the new physical sector address; and

3 (i) marking the physical sector address from the aforementioned
4 paragraph (b) as dirty.

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6 23. **(Original)** The method as recited in Claim 22, wherein the data structure
7 is contained in at least one memory device other than the flash memory medium.

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10 24. **(Original)** The method as recited in Claim 22, wherein the data structure
11 is contained in a random access memory device.

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13 25. **(Original)** The method as recited in Claim 22, wherein the requests are
14 received from a file system.

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17 26. **(Original)** The method as recited in Claim 22, wherein the data structure
18 is maintained by a flash abstraction logic of a flash memory driver.

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20 27. **(Canceled)**

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22 28. **(Original)** A system, comprising:

23 flash medium logic, configured to store data in a physical sector of a flash
24 memory medium;
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1 a table, configured to map logical sector addresses received from a file
2 system to physical sector addresses on the flash memory medium; and

3 flash abstraction logic, configured to ascertain a next free physical sector on
4 a flash memory medium and assign an address associated with the free physical
5 sector to a logical sector address associated with a write request received from the
6 file system.

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8 29. **(Original)** The system as recited in Claim 28, wherein the flash
9 abstraction logic is further configured to update the map of the logical sector
10 addresses to the physical sector addresses, after assigning the address associated
11 with the free physical sector to the logical sector address associated with the write
12 request.
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15 30. **(Original)** The system as recited in Claim 28, wherein the flash medium
16 logic marks a physical sector address as dirty after contents associated with the
17 physical sector address are rewritten to a new physical sector address.
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20 31. **(Original)** The system as recited in Claim 28, wherein the flash medium
21 logic is further configured to store the logical sector address within a portion of the
22 physical sector associated with the write request.
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32. **(Original)** The system as recited in Claim 28, wherein the flash medium logic is further configured to:

store the logical sector address within a portion of the physical sector associated with the write request; and

scan the flash memory medium for the portion of the physical sector storing the logical sector address after initialization of the system.

33. **(Original)** The system as recited in Claim 28, wherein the table is contained in a memory device other than the flash memory medium.

34. **(Original)** The system as recited in Claim 28, wherein the table is contained in a random access memory device.

35. **(Original)** The system as recited in Claim 28, wherein the system is a flash driver system.

36. **(Currently Amended)** A computer-readable medium for a flash driver, comprising computer-executable instructions that, when executed, direct the flash driver to:

receive a request to write data to a logical sector address of a flash memory medium;

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1 selecting a physical sector address from a list of free physical sector
2 addresses;
3 assign [[a]] the selected physical sector address to the logical sector address
4 forming a corresponding relationship between the addresses;
5 store the corresponding relationship between the addresses in a table; and
6 write the data into a physical sector of the flash memory medium at a
7 location indicated by the physical sector address.

10 37. **(Original)** A computer-readable medium for a flash driver, comprising
11 computer-executable instructions that, when executed, direct the flash driver to:

12 receive a request to write data to a logical sector address of a flash memory
13 medium;

14 assign a physical sector address to the logical sector address forming a
15 corresponding relationship between the addresses;

16 store the corresponding relationship between the addresses in a table;

17 write the data into a physical sector of the flash memory medium at a
18 location indicated by the physical sector address;

19 write the logical sector address in the physical sector of the flash memory
20 medium along with the data;

21 if the table is erased, then scan the flash memory medium to locate the
22 logical sector address stored with the data;
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1 assign the physical sector address containing the data to the logical sector
2 address forming a reestablished corresponding relationship between the addresses;
3 and
4 store the reestablished corresponding relationship between the addresses in
5 a new table.

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7 38. **(Currently Amended)** A system for tracking sectors in a flash memory
8 medium, comprising:
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10 means for receiving a request to retrieve data stored in the flash memory
11 medium from a location indicated by a logical sector address;

12 means for locating a physical sector address corresponding to the specific
13 logical sector address ~~from~~ in a table; and

14 means for reading the data stored in the flash memory medium from the
15 physical sector address ~~retrieved~~ located from the table.
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19 39. **(Original)** The system as recited in Claim 38, further comprising:

20 means for storing the logical sector address with the data in the flash
21 memory medium at a location indicated by the physical sector address;

22 means for reestablishing a portion of the table in the event of a power
23 interruption, by scanning the physical sector address for the associated logical
24 sector address; and
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1 means for storing the logical sector address in the table at a location
2 corresponding to the physical sector address.

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4 40. **(Original)** The system as recited in Claim 38, wherein the request is
5 received from a file system.

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7 42. **(Original)** The system as recited in Claim 38, wherein the table is
8 maintained by a flash abstraction logic of a flash driver.

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11 43. **(Original)** The system as recited in Claim 38, wherein the means for
12 reading of the data is performed by flash media logic of a flash driver.

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14 44. **(Original)** The system as recited in Claim 38, wherein the memory
15 device is a type of random access memory.

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18 45. **(Withdrawn)** A flash driver system, comprising:
19 a free sector manager, configured to determine a next free physical sector
20 address on the media and assign the address to a logical sector address of a write
21 request received from a file system;

22 a table, configured to store a map showing the assignment of the physical
23 sector address to the logical sector address; and
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1 a flash medium logic, configured to write the data to the next free physical
2 sector indicated by the free sector manager and store the logical sector address
3 directly with the data on the flash memory medium.
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5 46. **(Withdrawn)** The flash driver as recited in Claim 45, further
6 comprising a flash abstraction logic, configured to scan the sectors of the flash
7 memory medium for the logical sector address and note the physical sector address
8 from which the logical sector address is stored and reestablish the map in the table
9 in the event the table is erased.
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12 47. **(Withdrawn)** The flash driver as recited in Claim 45, wherein the
13 logical sector address is stored in a spare portion of the flash memory medium.
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15 48. **(Withdrawn)** A flash driver system, comprising:
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17 a free sector manager, configured to determine a next free physical sector
18 address available on the flash memory medium;
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20 a flash abstraction logic, configured to query the free sector manager for the
21 next free physical sector address and link the physical sector address to a logical
22 sector address received from a file system; and
23
24 a table, configured to store the physical sector address to logical sector
25 address linking performed by the flash abstraction logic.

49. **(Withdrawn)** The flash driver system as recited in Claim 48,
wherein the flash abstraction logic is further configured to mark an existing
physical sector as dirty, if a logical sector address received from the file system
was previously linked to the logical sector address in the table.

50. **(Withdrawn)** The flash driver system as recited in Claim 48,
wherein the flash abstraction logic is further configured to mark an existing
physical sector as dirty, if a logical sector address received from the file system
was previously linked to the logical sector address in the table, but only after data
associated with the logical sector address is successfully written to a new physical
sector address on the flash memory media.

51. **(Withdrawn)** The flash driver system as recited in Claim 48,
wherein a portion of a physical sector is a status bit configured to indicate when a
write operation is in progress.

52. **(Withdrawn)** The flash driver system as recited in Claim 48,
wherein a portion of physical sector is a status bit configured to indicate when a
write operation has been completed successfully.